

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Norio FUKASAWA et al

Serial Number: 09/766,656

Filed: January 23, 2001



PTO Confirmation No. 1144

Group Art Unit: 2814

Examiner: Graybill, D.

For: **METHOD AND MOLD FOR MANUFACTURING
SEMICONDUCTOR DEVICE, SEMICONDUCTOR
DEVICE AND METHOD FOR MOUNTING THE DEVICE**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

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JUN 23 2003
TECHNOLOGY CENTER 2800

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 19, 2003

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of these documents is attached.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits or expiration of the three month period following the filing date or the entry of the national stage of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can, in the first instance, determine any materiality thereof to the claimed invention. See 37 CFR 1.104(a) concerning the PTO duty to consider and use any such information. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the

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